

**Docket No.**  
**03 P 52507 US (90035.000211)**

**IN THE UNITED STATES PATENT & TRADEMARK OFFICE**

Applicant :	V. K. JaiPrakash, et al.	)	Examiner:
		)	TBA
Serial No. :	TBA	)	
		)	
Filed :	Filed Herewith	)	Art Unit
		)	Unknown
For :	DRAM VERTICAL TRANSISTOR AND TRENCHCAPACITOR MEMORY CELLS AND METHOD OF FABRICATION	)	
		)	
		)	

**PRELIMINARY AMENDMENT**

Mail Stop Patent Application  
Commissioner For Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Preliminary to examination of the above-referenced patent application, please make the following amendments.